

## Computer System Architecture Rao Pvs

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P. V. S. Rao. Paranandi Venkata Suryanarayana Rao is an Indian computer scientist, known for his researches in the fields of speech and script recognition and is credited with contributions for the development of TIFRAC, the first indigenously developed electronic computer in India. He is a recipient of awards such as IEEE Third Millenium Medal, Vikram Sarabhai Award, Om Prakash Bhasin Award and VASVIK Industrial Research Award.

## ~~P. V. S. Rao — Wikipedia~~

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Rao, A. S., Georgeff, M. P. (1993) A model-theoretic approach to the verification of situated reasoning systems. In: Proceedings of the Thirteenth International Joint Conference on Artificial Intelligence (IJCAI-93), Chamberey, France.

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This paper presents a novel virtual-channel (VC) sharing technique for NoC architecture. The proposed architecture improves the utilization of resources to enhance the performance with minimal overheads. A heuristic approach towards the proper VC sharing strategy is proposed, which is performed by an adaptive algorithm that configures the VC sharing based on link load parameters.

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